

SHREE DATTEA POLYTECHNIC COLLEGE, DATTANAGER, SHIROL.

CLASS TEST -01 modal answer

Subject- PED

Time- 1HR

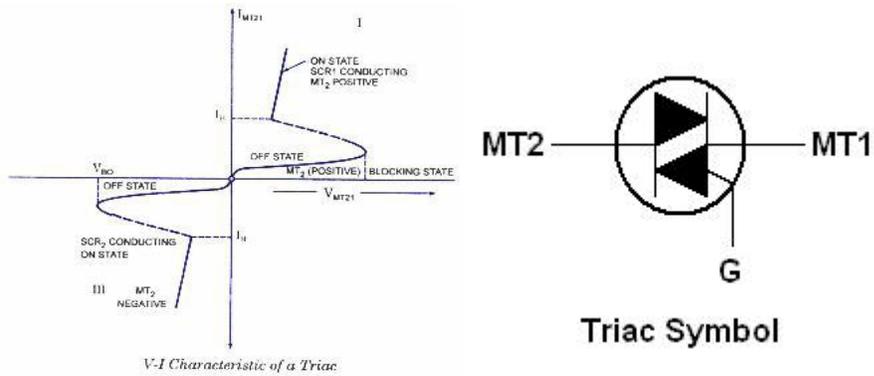
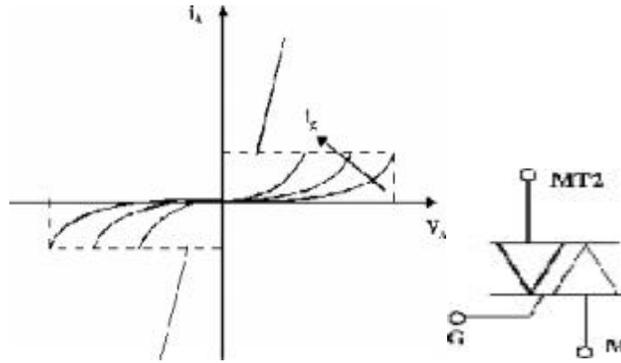
Subject Code- 12232

Marks- 25

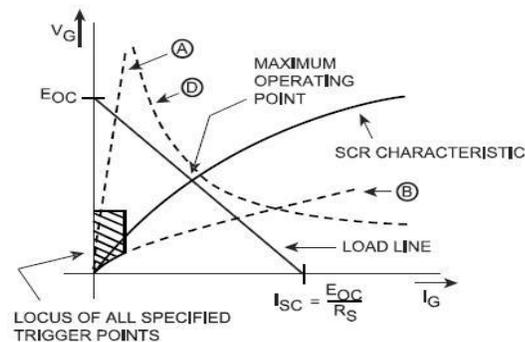
Q1. Attempt any three

09

a) Draw a symbol and V-I characteristics of SCR and TRIAC



b) Draw and explain D.C. gate characteristics.



Gate characteristics is graph of V_g and I_g . i.e. forward gate voltage and gate current dc values. As the gate cathode junction is a PN junction, the characteristics are same as that of a diode. For a particular type of SCRs, V_g - I_g characteristics is spread between two curves A and B. Curve D represents the P_{gav} curve. The operating point is the intersection of gate circuit source line and the thyristor characteristics. The intersection should be as close to P_{gav} curve and lie within A and B for reliable turn ON of thyristor.,

c) List and explain any six specifications of thyristor.

CURRENT RATINGS OF SCR

- Surge Current Rating (IFM)—The surge current rating (IFM) of an SCR is the peak anode current an SCR can handle for a short duration.
- Latching Current (IL)—A minimum anode current must flow through the SCR in order for it to stay ON initially after the gate signal is removed. This current is called the latching current (IL).
- Holding Current (IH)—After the SCR is latched on, a certain minimum value of anode current is needed to maintain conduction. If the anode current is reduced below this minimum value, the SCR will turn OFF.

VOLTAGE RATINGS OF SCR

- Peak Repetitive Reverse Voltage (VRRM)—The maximum instantaneous voltage that an SCR can withstand, without breakdown, in the reverse direction.
- Peak Repetitive Forward Blocking Voltage (VDRM)—The maximum instantaneous voltage that the SCR can block in the forward direction. If the VDRM rating is exceeded, the SCR will conduct without a gate voltage.
- No repetitive Peak Reverse Voltage (VRSM)—The maximum transient reverse voltage that the SCR can withstand.
- Maximum Gate Trigger Current (IGTM)—The maximum DC gate current allowed to turn the SCR ON.

d) Define following terms:

i) Firing angle:- it is defined as an angle between the instant at which the thyristor would conduct firing angle may be measured with respect to given reference at which the firing pulse are given to SCR

ii) Extension angle:- it is defined as the angle measured from the reference point to the instant at which the current extinguishes to zero

e) What is free-wheeling diode? State its advantages.

Freewheeling diode is a commutating device because it transfers the load current away from the rectifier circuit this is also known as a bypass diode.

Advantages of freewheeling diode.

- 1) load current wave form is improved
- 2) it prevents negative voltage swing
- 3) it is with RL load is identical equal to R load
- 4) it improves the input power factor of the system

Q2. Attempt any two

08

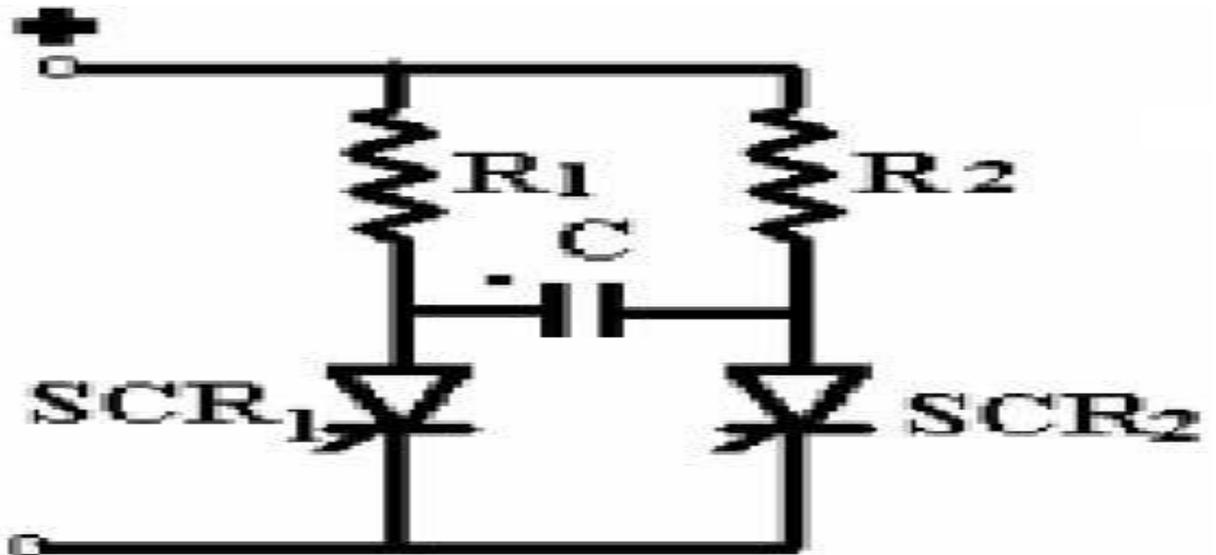
a) List and explain any six specifications of thyristor.

CURRENT RATINGS OF SCR

- Surge Current Rating (IFM)—The surge current rating (IFM) of an SCR is the peak anode current an SCR can handle for a short duration.
- Latching Current (IL)—A minimum anode current must flow through the SCR in order for it to stay ON initially after the gate signal is removed. This current is called the latching current (IL).
- Holding Current (IH)—After the SCR is latched on, a certain minimum value of anode current is needed to maintain conduction. If the anode current is reduced below this minimum value, the SCR will turn OFF.

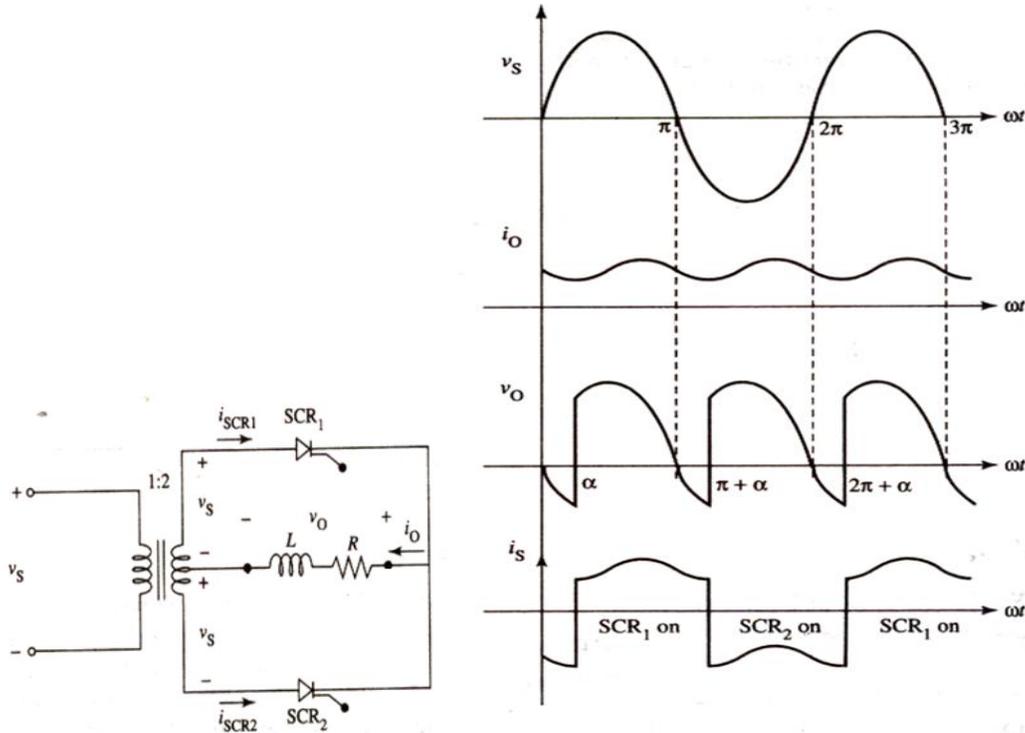
VOLTAGE RATINGS OF SCR

- Peak Repetitive Reverse Voltage (VRRM)—The maximum instantaneous voltage that an SCR can withstand, without breakdown, in the reverse direction.
- Peak Repetitive Forward Blocking Voltage (VDRM)—The maximum instantaneous voltage that the SCR can block in the forward direction. If the VDRM rating is exceeded, the SCR will conduct without a gate voltage.
- Non-repetitive Peak Reverse Voltage (VRSM)—The maximum transient reverse voltage that the SCR can withstand.
- Maximum Gate Trigger Current (IGTM)—The maximum DC gate current allowed to turn the SCR ON.



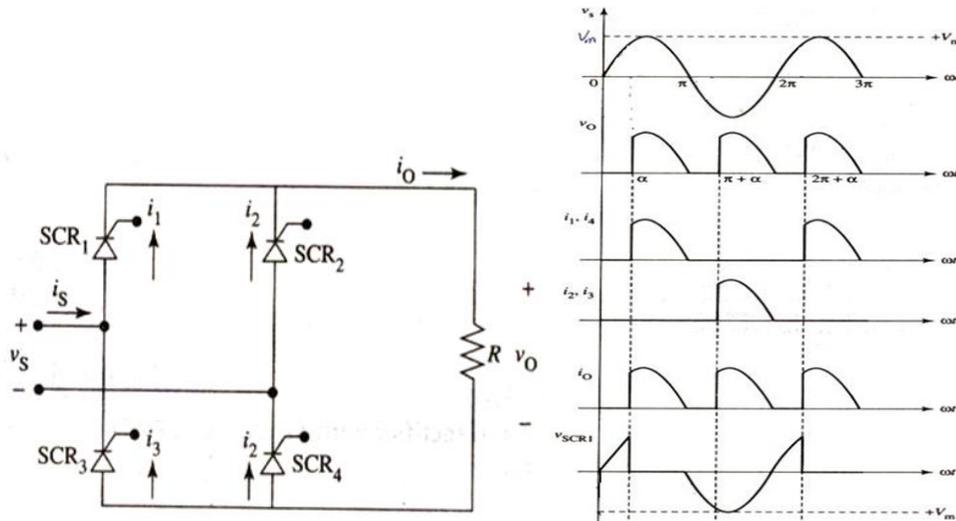
This configuration has two **SCRs**. One of them may be the main **SCR** and the other auxiliary. Both may be load current carrying main **SCRs**. The configuration may have four **SCRs** with the load across the capacitor, with the integral converter supplied from a current source. Assume **SCR2** is conducting. C then charges up in the polarity shown. When **SCR1** is triggered, C is switched across **SCR2** via **SCR1** and the discharge current of C opposes the flow of load current in **SCR2**.

- a) With neat circuit diagram and waveform explain the operation of single phase full wave controlled rectifier with RL load.



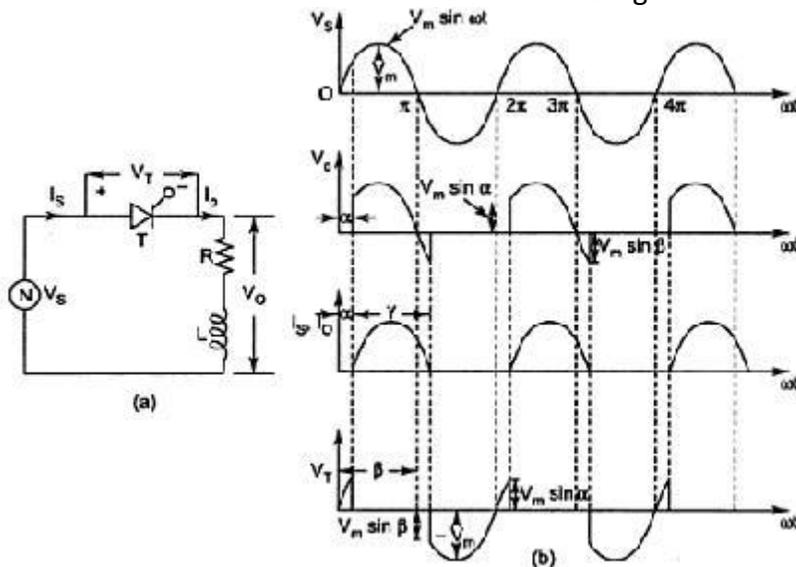
During positive-half cycle of source voltage, SCR₁ is forward biased and SCR₂ is reverse biased. When firing angle is given to the scr 1 it starts the delivering voltage and current to the load from $wt = \alpha$ to β scr 1 is not turn off at $wt = \pi$ due to presence of inductor in load side. During negative half-cycle, SCR₂ is forward biased and SCR₁ is reverse biased. When firing angle is given to the scr 2 it starts the delivering voltage and current to the load from $wt = \beta$ to $2\pi + \alpha$ scr 1 is not turn off at $wt = 2\pi$ due to presence of inductor in load side.

- b) With neat circuit diagram and waveform explain the operation of single phase full wave bridge controlled rectifier with R load.



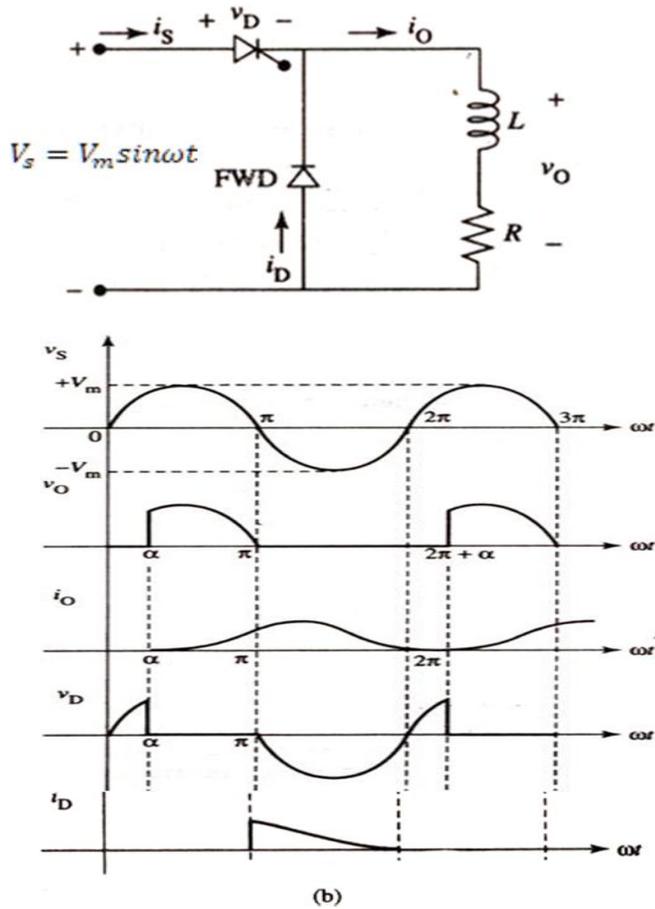
During positive-half cycle of source voltage, SCR1 and SCR 2 is forward biased and SCR3 and SCR 4 is reverse biased. When firing angle is given to the SCR1 and SCR 2 it starts the delivering voltage and current to the load from $wt=\alpha$ to π SCR1 and SCR 2 is turn off at $wt=\pi$. During negative half-cycle SCR3 and SCR 4 is forward biased and SCR1 and SCR 2 is reverse biased. When firing angle is given to the SCR3 and SCR 4 it starts the delivering voltage and current to the load from $wt=\beta$ to 2π SCR3 and SCR 4 is turn off at $wt=2\pi$

- c) With neat circuit diagram and waveform explain the operation of single phase half wave controlled rectifier with RL load and free-wheeling diode.



The above figure shows operation of half converter with RL load. As it can be seen that thyristor continues to conduct after $wt=\pi$ as the current has not become zero. This is because there is stored energy in the inductance. This causes drop in the average output voltage as it has

negative going period after $\omega t = \pi$. If the inductance is large, trigger circuit loses its control on the thyristor. This situation is overcome by forcibly turning off the thyristor at $\omega t = \pi$. This can be achieved by connecting a diode across load which is called as a freewheeling diode. Which is shown in the fig. below.



When supply voltage tends to reverse, it will forward bias the freewheeling diode and the load voltage will be zero at $\omega t = \pi$.